



Enhanced fracture strength of thin wafers and chips due to Laser-Microjet technology

Bernold Richerzhagen

Delphine Perrottet

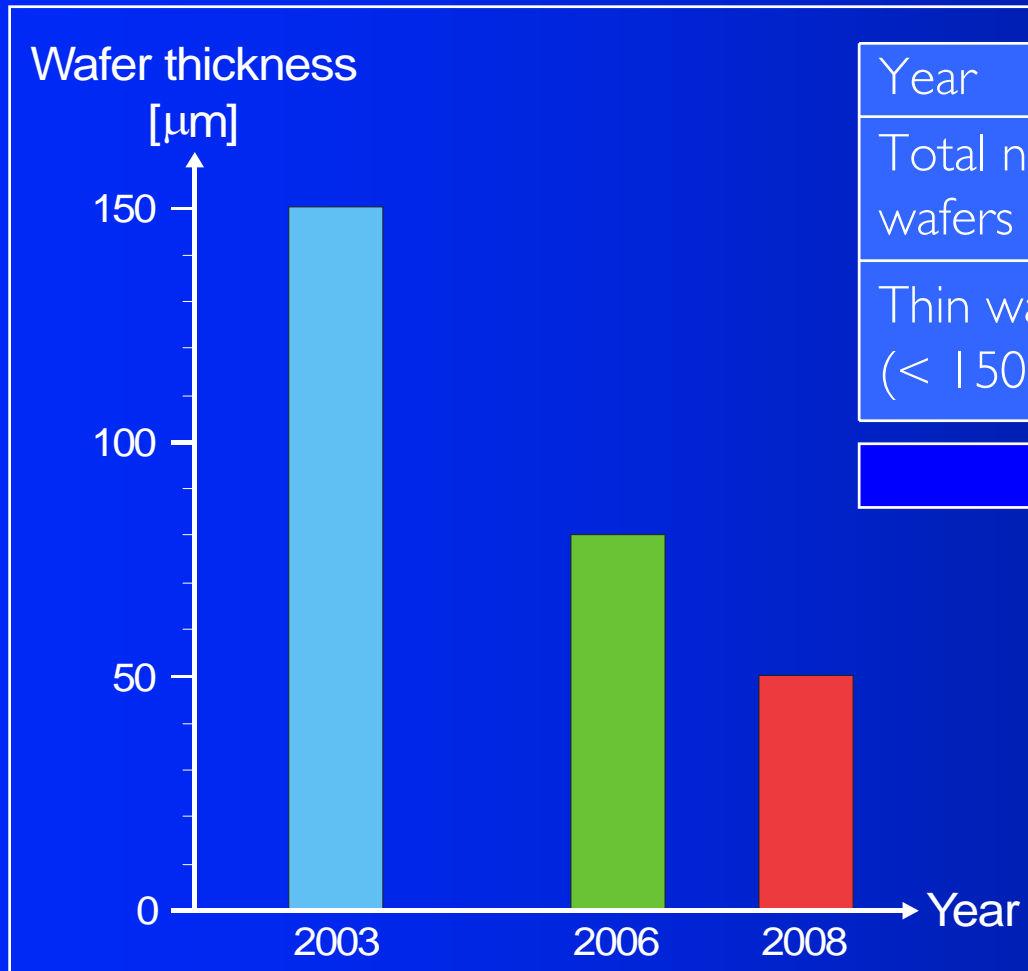
Roy Housh

John Manley*

Synova SA, Ecublens, Switzerland

*Synova-USA Inc.

Thin Wafers – Market Trend



Year		2004	2006	2008
Total number of wafers [millions]		140	140	140
Thin wafers (< 150 µm)	%	< 5%	20 %	30 %
	#	< 7	28	42

*Expected market evolution of thin wafers**

Use of thin wafers for:

- Smart cards
- Stacked packages
- Diodes



The market of thin wafers is expanding!

* Fraunhofer-Institute for Mechanics of Materials 2004

Issues with Thin Wafers

Problems on wafer level

Damage due to grinding

Warping of wafer

Cracking

Wafer breakage

Difficult handling

Problems on die level

Damage due to dicing

Chipping

Micro-cracks

Die breakage

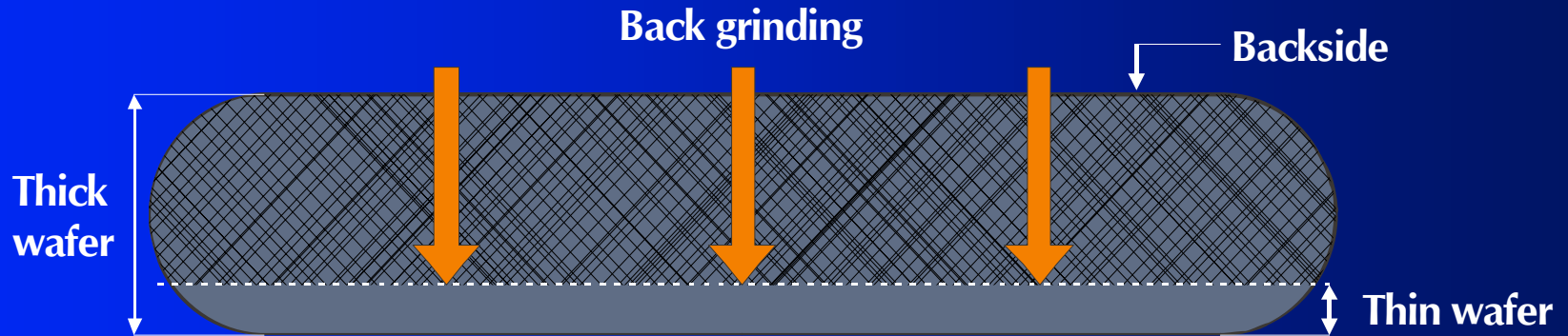
Difficult handling

Low fracture strength



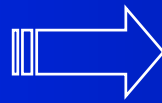
New solutions are needed!

Back Grinding Damages



Problem

Back grinding induces **damages** on wafers



During handling, high risk of cracks propagation resulting in wafer **breakage**

Solution

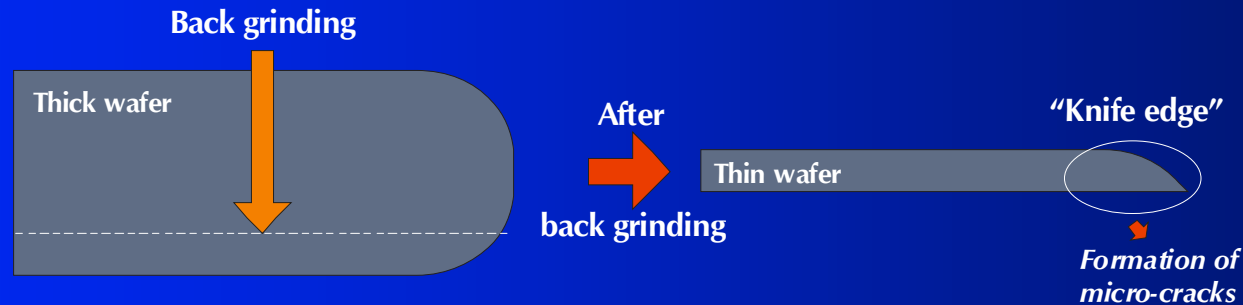
Stress release method after back grinding



Improved **wafer surface** strength

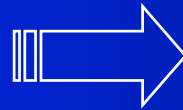
Method	CMP	Wet etching	Dry etching	Dry polishing
Speed	Low	High	Low	Low
Productivity	Low	High	High	Medium
Running cost	High/Medium	High	Low	Very Low

Edge Damages



Problem

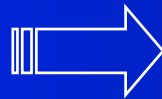
Grinding induces damages on edge



- Wafer breakage
- Irregular, undefined edge
- Micro-cracks

Solution

Today, no technology is able to grind or polish the sensitive edge of thin wafers

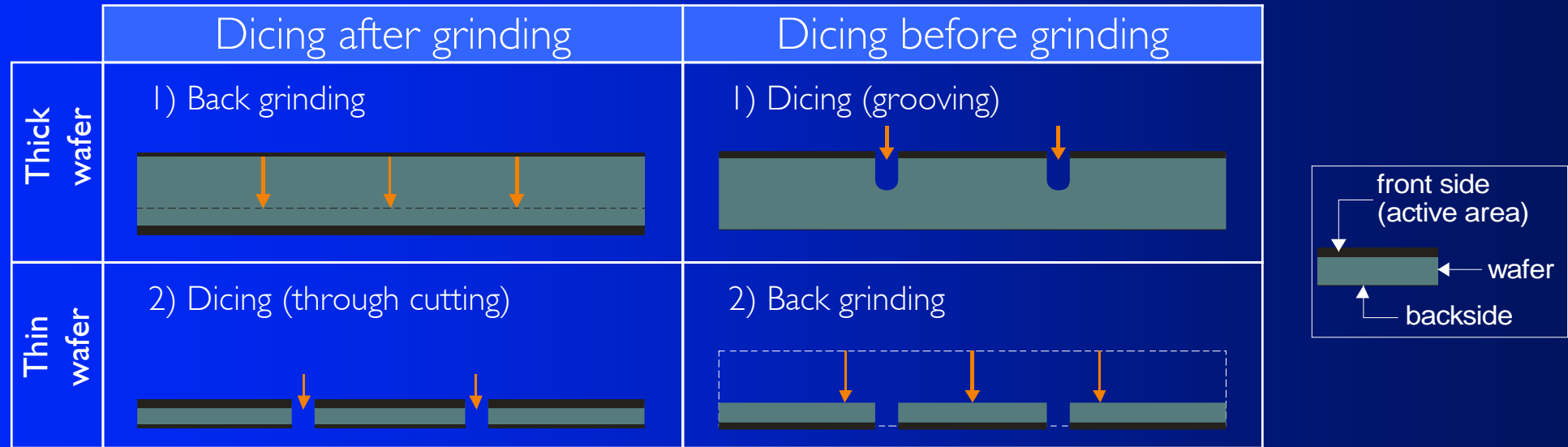


New damage-free edge grinding technology



Improved **wafer edge** strength

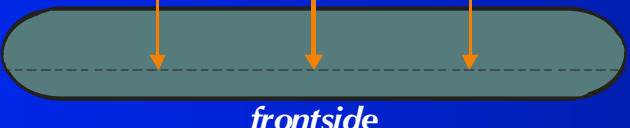
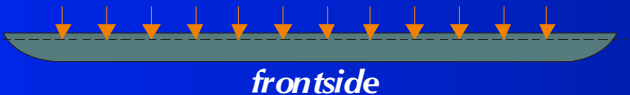


Dicing Damages



Problem	<p>Dicing induces damages on wafers</p>	<ul style="list-style-type: none"> ➤ Low die edge fracture strength ➤ Chipping ➤ Micro-cracks
----------------	---	--

Solution	<p>Today, conventional processes are not able to dice thin wafers</p>	<p>New damage-free dicing technology</p>	<p>Improved die edge strength</p>
-----------------	---	--	--

Complete solution for thin wafers

1	Back Grinding	<p style="text-align: center;">Grinder</p>  <p style="text-align: center;"><i>frontside</i></p>	<ul style="list-style-type: none"> • Reduces wafer thickness → thin wafer • Generates micro-cracks and stress
2	Stress Release	<p style="text-align: center;">Stress release method</p>  <p style="text-align: center;"><i>frontside</i></p>	<ul style="list-style-type: none"> • Polishes the thin wafer surface (backside) • Removes surface stress
3	Edge Grinding	<p style="text-align: center;">Laser-Microjet</p>  <p style="text-align: center;"><i>backside</i></p>	<ul style="list-style-type: none"> • Takes off the thin wafer edge • Removes micro-cracks at the edge
4	Other Backside Processes		
5	Dicing	<p style="text-align: center;">Laser-Microjet</p>  <p style="text-align: center;"><i>backside</i></p>	<ul style="list-style-type: none"> • Singulates the wafer into chips • Generates no additional damages

Edge Grinding – After Back Grinding

Patent
pending

Cutting off the edge of the thin wafer

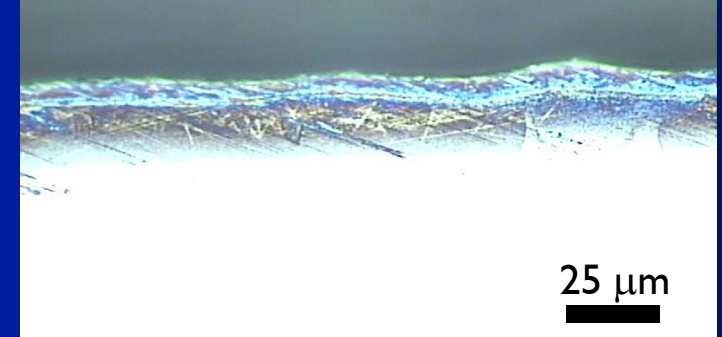
LMJ edge grinding

Thin wafer

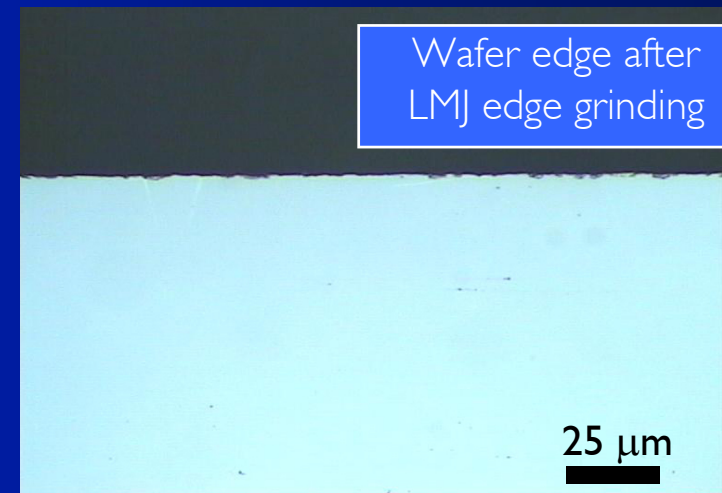


- The **knife-edge** containing the micro-cracks all around the wafer is **removed**
- Wafer **breakage** during the final steps (especially handling) is strongly reduced
- Edges are very **clean and well defined**
- **No** new **micro-cracks** in the edges

Wafer edge after
back grinding



Wafer edge after
LMJ edge grinding

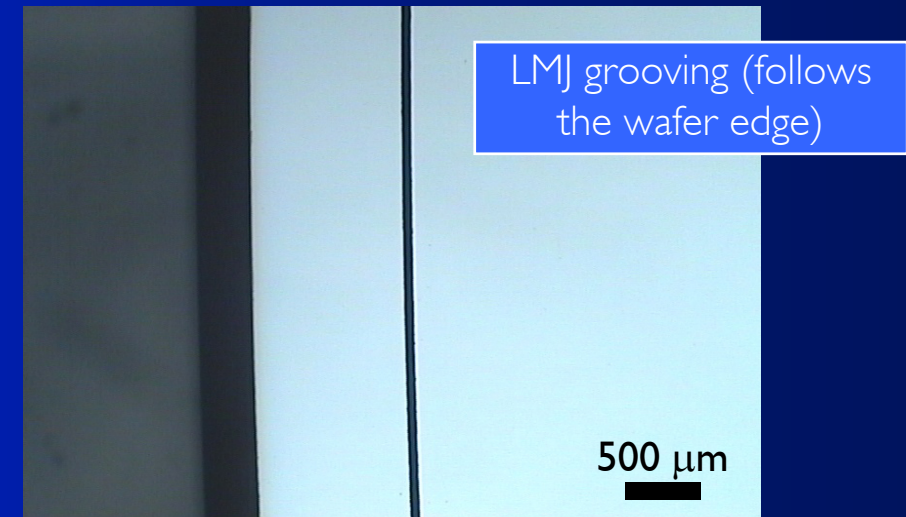
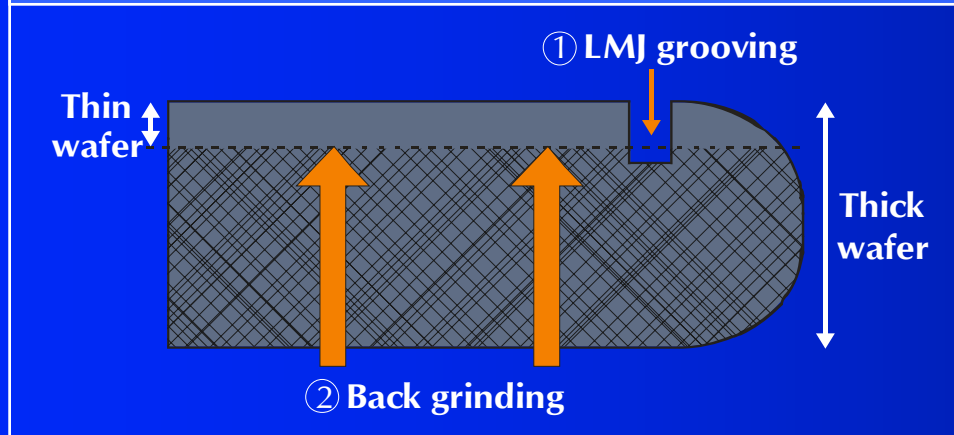


(100-μm thick silicon wafer)

Edge Grinding – Before Back Grinding

Patent
Pending

Stopping the cracks before back grinding,
on the thick wafer

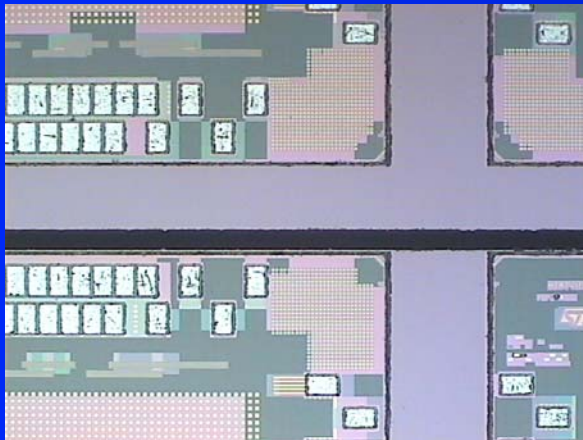


Grooving of a 750-μm thick silicon wafer

- **Reduced micro-cracks** since the sensitive edge is removed
- Wafer **breakage** reduced
- Very **clean** and **well defined edge**

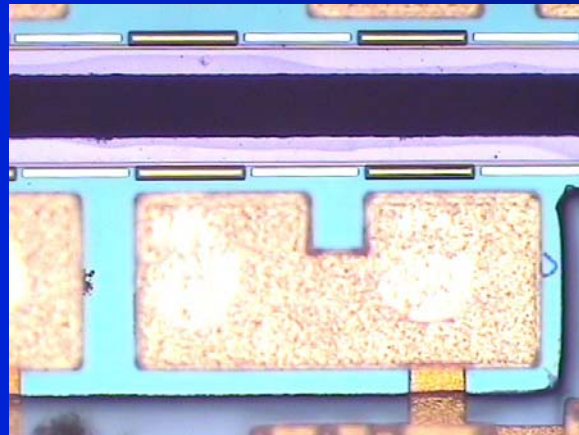
Laser-Microjet Dicing

Patented



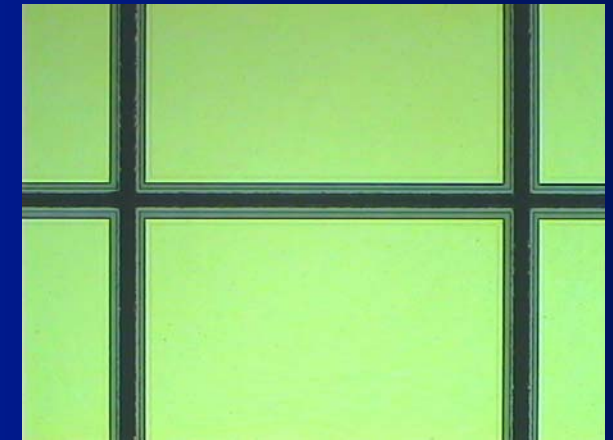
Low-k wafer

- Wafer thickness: 100 μm
- Through cutting
- Kerf width: 30 μm
- Cutting speed: 50 mm/s



GaAs wafer

- Wafer thickness: 100 μm
- Through cutting
- Kerf width: 26 μm
- Cutting speed: 60 mm/s



SiC wafer

- Wafer thickness: 100 μm
- Through cutting
- Kerf width: 23 μm
- Cutting speed: 20 mm/s

➔ The water jet guided laser is **the** solution for any kind of semiconductor materials!

Advantages over conventional dry lasers

Laser-Microjet[®]

Through-cutting

Unlimited wafer thickness (up to 20 mm)

Maintenance-free fiber lasers

Robust, field-proven IR lasers

High dicing speed of up to 300 mm/s
(for 50- μ m thick Si)

Parallel kerf

Omnidirectional cutting (hexagonal chips,
round chips, rounded corners...)

No thermal damages, thanks to the waterjet

Conventional UV (or IR) Laser

Scribing mainly, as through-cutting is slow

Limited wafer thickness

Maintenance costs (diodes)

Delicate lasers (UV)

Low scribing / dicing speed

V-shape kerf

Omnidirectional cutting (with single beam)

Thermal damages with any kind of dry laser

Advantages over conventional “dry” lasers

Laser-Microjet[®]

No contamination thanks to the use of a water film

No need of protective layer such as the conventional laser needs

Any semiconductor material (including III/V composites)

LaserTape[®] for through-cutting

Low-k wafer compatible

Highest fracture strength, higher than DBG with surface- and edge- stress release

Conventional UV (or IR) Laser

Always contamination; cutting through water impossible

Need of protective layer

Dangerous with toxic III/V materials (arsenoxides for example)

No tape available

Incompatible with low-k wafers

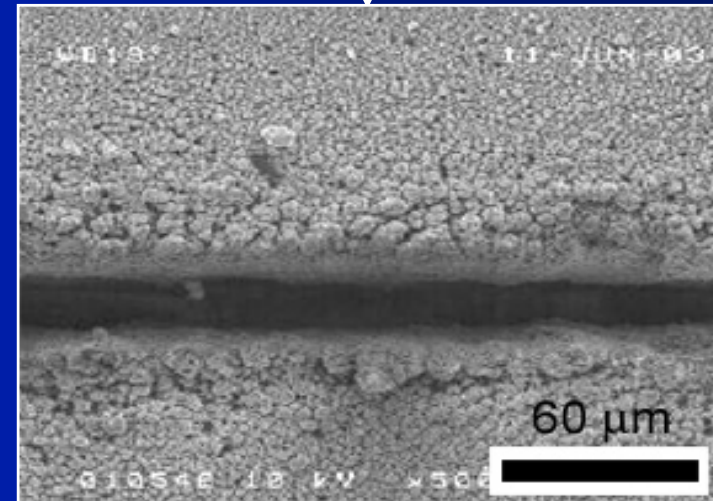
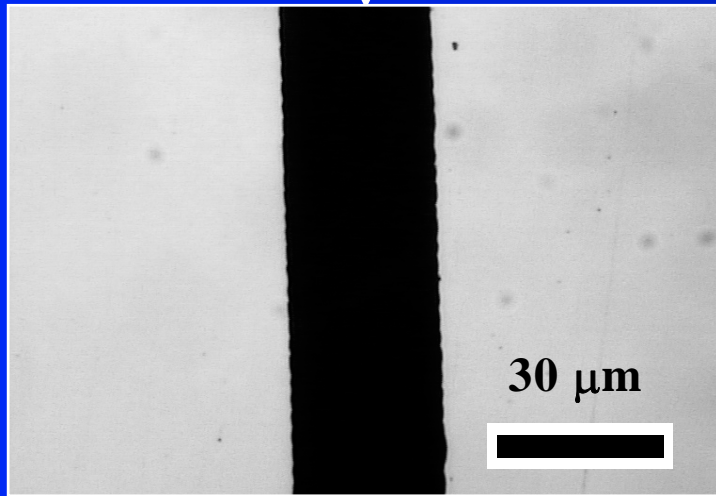
Poor fracture strength, lower than standard dicing

Advantages over conventional dry lasers

Laser-Microjet[®]

Conventional UV Laser

Through-cut in silicon (quality as cut)



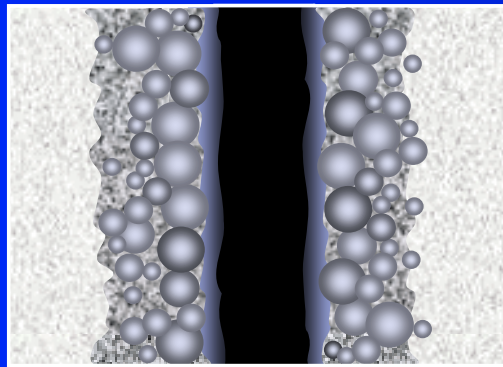
Wafer thickness	150 μm
Kerf width	26 μm
Cutting speed	80 mm/s

Wafer thickness	150 μm
Kerf width	10-30 μm
Cutting speed	6 mm/s

Advantages over conventional dry lasers

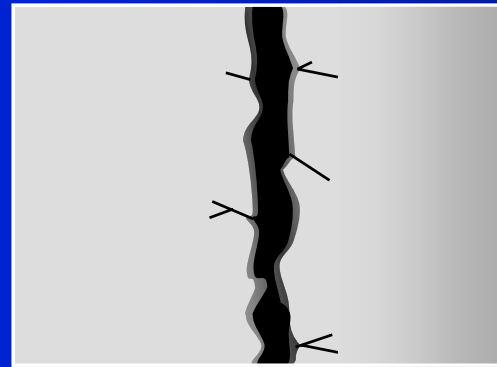
Conventional UV Laser

Front-side



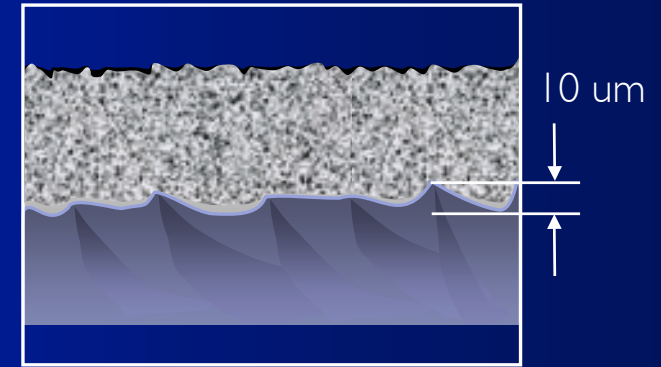
33 μm

Back-side



14 μm

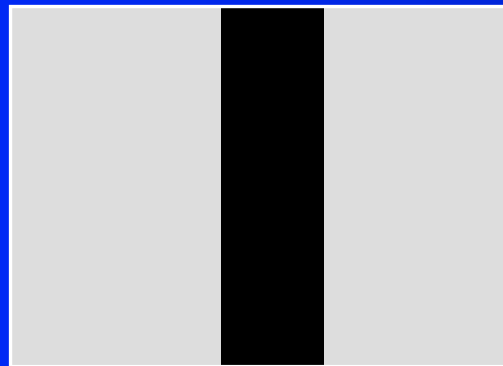
Edge



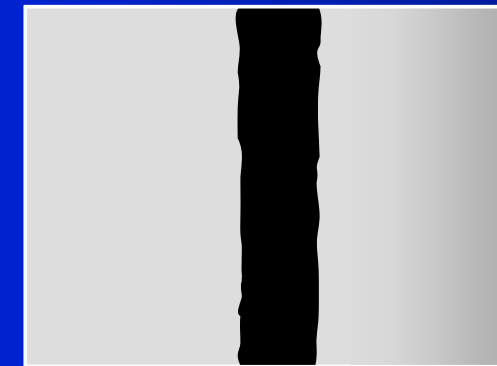
10 μm

LaserMicrojet®

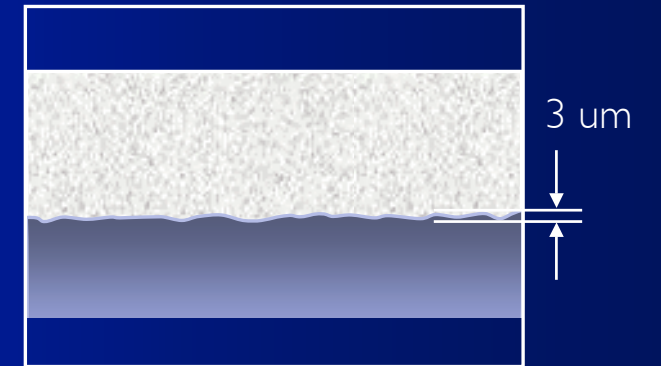
38 μm



30 μm



3 μm



Fracture Strength Test - Setup

3-beam bending test



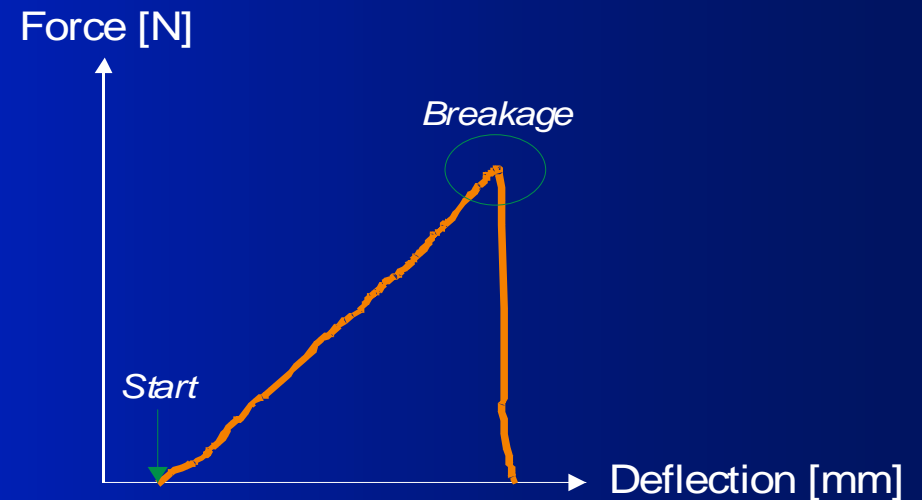
→ measuring the influence of edge damage

Samples

- 125- μm thick production silicon wafers
- same batch
- ground and etched

Process

1. Dicing into 10 \times 10 mm chips
 2. Testing on 3-beam bending set up
- (around 25 dies were tested for each parameter)

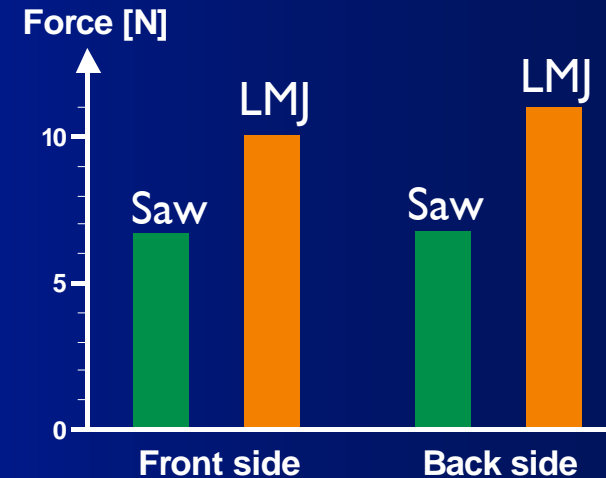


High force → High die fracture strength

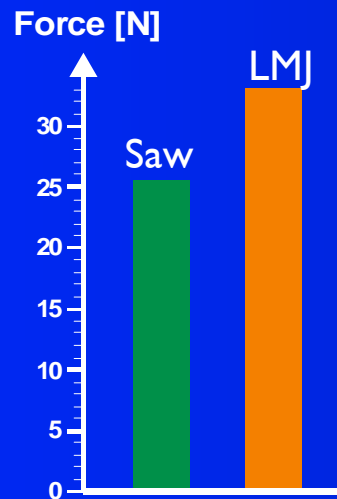
Fracture Strength Tests - Results

1) Tests from Synova SA

Dicing technology	Dicing speed	Kerf width	Fracture strength (average force [N])	
			Front side	Back side
■ Saw	25 mm/s	45 μm	X (6.7)	Y (6.8)
■ LMJ	50 mm/s	46 μm	1.5 X (10.1)	1.6 Y (11)



2) Tests from Infineon Technologies AG

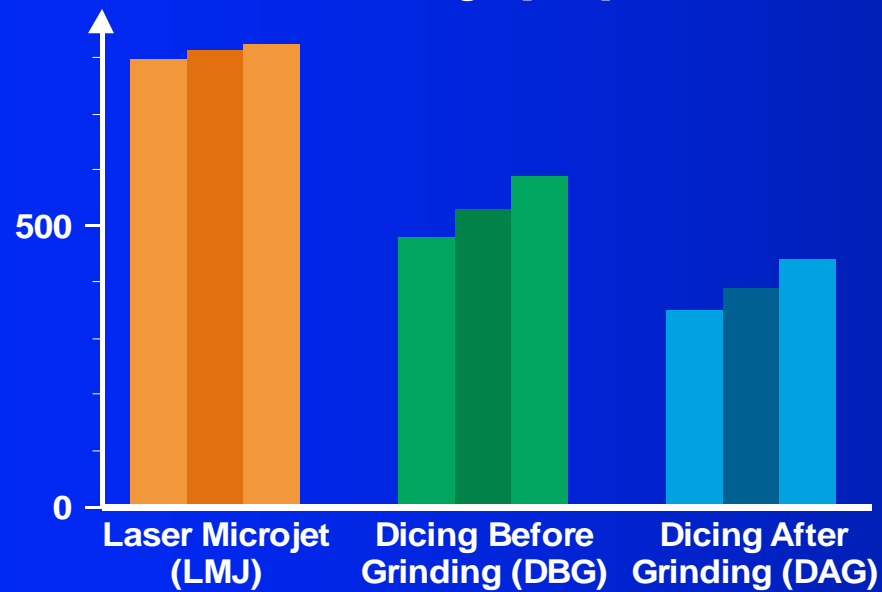


Dicing technology	Fracture strength (average force [N])
■ Saw	X (25.5)
■ LMJ	1.3 X (33.2)

Fracture Strength Tests - Results

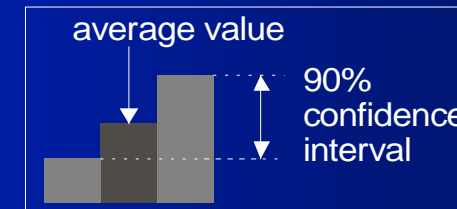
3) Tests from Fraunhofer-Institute for Mechanics of Materials

Characteristic Weibull strength [MPa]



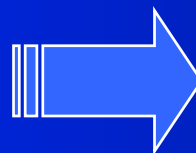
Dicing technology	Average Weibull strength [MPa]
DAG	X (390)
DBG	1.3 X (530)
LMJ	2 X (810)

1.5



Conclusions

Same results from
3 different analysis



**Laser-Microjet process allows
highest die fracture strength**

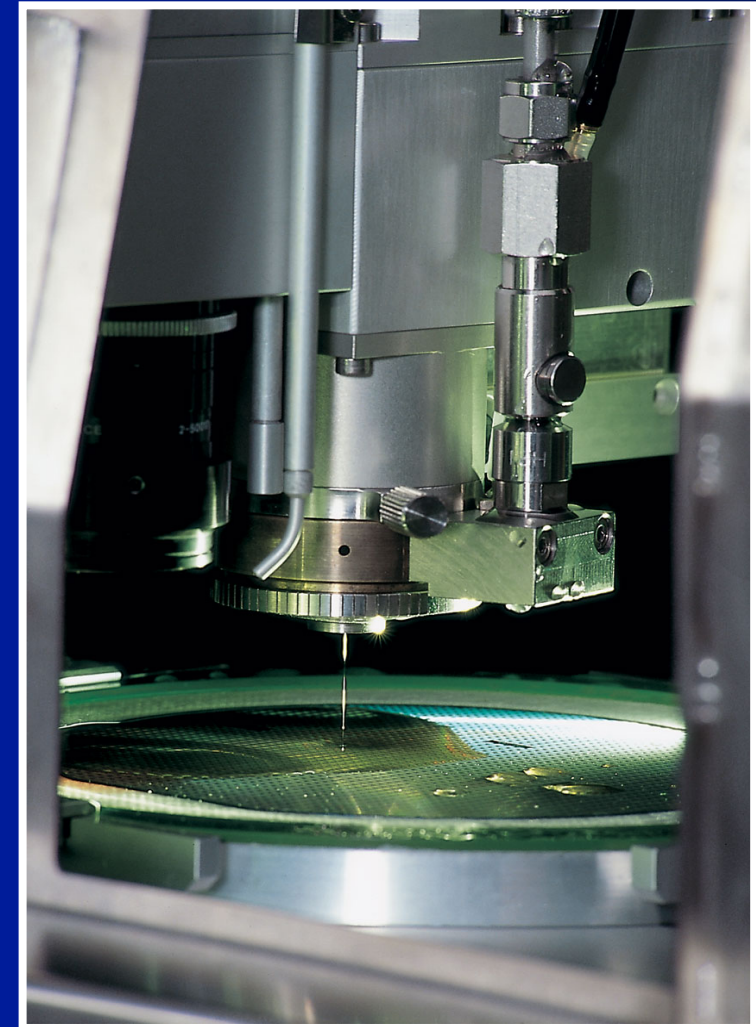
Conclusions

Operations on thin wafers

- ✓ Edge grinding
- ✓ Dicing



Laser-Microjet process allows a perfect processing of thin wafers and dies without damages, **generating wafers and dies with highest fracture strength!**



The Water Jet Guided Laser

